

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application for:

Steven Teig

Serial No.: 09/742,171

Filing Date: 12/19/2000

For: METHOD AND APPARATUS FOR

QUANTIFYING THE QUALITY OF PLACEMENT CONFIGURATIONS IN A PARTITITIONED REGION OF AN INTEGRATED CIRCUIT LAYOUT Examiner: Thompson, Annette

Group Art Unit: 2825

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may expandize the validity of the application or any patent issued thereon.

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